

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	1174631
<b>Application Number:</b>	10595567
<b>Confirmation Number:</b>	2837
<b>Title of Invention:</b>	SEMICONDUCTOR INTEGRATED CIRCUIT AND DESIGN METHOD THEREOF
<b>First Named Inventor:</b>	Kiyoshi KATO
<b>Customer Number:</b>	26171
<b>Filer:</b>	John F. Hayden/Andrea Turner
<b>Filer Authorized By:</b>	John F. Hayden
<b>Attorney Docket Number:</b>	12732-335US1
<b>Receipt Date:</b>	28-AUG-2006
<b>Filing Date:</b>	
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<b>Application Type:</b>	U.S. National Stage under 35 USC 371
<b>International Application Number:</b>	

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Information Disclosure Statement (IDS) Filed	12732-335US1-ids.pdf	113100	no	3

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This is not an USPTO supplied IDS fillable form

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113100

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**New Applications Under 35 U.S.C. 111**

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

**National Stage of an International Application under 35 U.S.C. 371**

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.